



UNIVERSITY OF NORTH TEXAS

COMPUTER SCIENCE &

ENGINEERING

STUDENT NEWSLETTER

UNT
CSE

CSE

April 2015 Edition

Department of Computer Science and Engineering News

CSE Students to present projects on Design Day on April 24

CSE hosts Robocamp in Summer 2015

UNT Center for Information and Computer Security hosts Workshop on Hot Topics in Networking and Security (HoNeST)

NCSS I/UCRC Spring 2015 meeting

Dr. Bryant and CSE Students attend Diversity in Computing Conference

Computational Epidemiology Research Laboratory News

News from Computer Systems Research Laboratory

Laboratory for Recreational Computing News

News from Software Engineering Language Laboratory

Prof. Mohanty publishes a Major Textbook with McGraw-Hill and initiates a New Letter for IEEE-CS

Student News

Outstanding CSE Students recognized

CSE Faculty recognized by Outstanding Students

CSE Students defend Dissertations and Theses

Junfei Xie wins Wanda J. Shafer Graduate Scholarship

CSE Graduate Students participate in 3rd Annual Graduate Exhibition

CSE Graduate Students place in top 25% at HackDFW

College of Engineering News

UNT's Society of Hispanic Professional Engineers to host Regional Leadership Conference

CENG Students invited to participate in Cold-formed Steel Structural Design Competition

Greetings from the CSE Chair

Dear CSE Students,

This Friday many of our senior undergraduate students will present their posters and presentations at the College of Engineering's annual Design Day. CSE Students in Computer Engineering and Information Technology capstone classes will present their posters in front of our department between 9 am and 11 am. After that at 11:30 am, each team of students will give presentations in D215. All team projects and team members are listed below. Faculty and students are invited to attend. In Spring 2016, Computer Science students will have a

Prof. Mohanty publishes a Major Textbook with McGraw-Hill and initiates a New Letter for IEEE-CS

Professor Saraju Mohanty has published a comprehensive textbook on Nanoelectronic **Nanoelectronic Mixed-Signal System Design** was published by McGraw-Hill under the ISBN: 978-0071825719 and 0071825711 in 2015. This is 800-page text with 700 illustrations comprehensively covers all aspects of Nanoelectronic VLSI Circuits and Systems.

The book discusses mixed-signal circuit and system design based on both existing nanoscale CMOS and emerging nanoelectronic technologies. The book presents the important issues, challenges, and solutions for digital, analog, and mixed-signal designs which have significant usage in daily applications like smart mobile phones. The key techniques which are required for design for excellence, power, variability, and manufacturability are discussed in this practice-driven text. It discusses design flow as well as simulation methods needed for Nanoelectronic VLSI Circuits and Systems.



The book is adopted by CSCE 4730/5470 as well as CSCE 6731 courses in the Department of Computer Science and Engineering at the UNT College of Engineering. The book will provide nanoelectronic system learning opportunity for hundreds of students at the College of Engineering at UNT. Prof. Mohanty has plans to make companion lecture slides available after approval from publisher in the future. Based on the feedback Prof. Mohanty has received from various conferences that he has attended, the book will have widely adopted at other universities in the USA, Europe and India.

Professor Mohanty has initiated a new letter for the Technical Committee on Very Large Scale Integration (**TCVLSI**, IEEE Computer Society (**IEEE-CS**)). The letter called "VLSI Circuits and Systems Letter" is meant for fast dissemination of finding new research, presenting opinions of leading researchers, publicizing various conferences, and engaging in education and outreach. The inaugural issues are available [HERE](#). Prof. Mohanty who chairs TCVLSI invites students and faculty to join TCVLSI. There is no fee to join. Click [HERE](#) to join TCVLSI.

In other news from NanoSystem Design Laboratory (**NSDL**), PhD student Umar Albalawi has cleared the qualifier examination and now becomes a PhD candidate. PhD student Shital Joshi is in the process of taking his qualifier examination. PhD student V. Prasanth Yanambaka travelled Santa Clara, CA to present the following paper: S. P. Mohanty, E. Kougianos, and V. P. Yanambaka, "Ultra-Fast Process-Aware Design Optimization of PLL using Bootstrapped Kriging and PSO", in Proceedings of the 16th International Symposium on Quality Electronic Design, pp. 239--242, 2015. ↑