

Dated: 23rd Nov 04, CSCI 4330 /5330: Digital Systems Design with VHDL
Instructor: Saraju P. Mohanty

- **30th Nov (Tue):** Test3
- **2nd Dec (Thu):** Last lab session
- **7th Dec (Tue):** Submission of Last lab reports, Graded Test3 return (pls. get any grading related issue clarified today), and Graduate student's presentation.
- **9th Dec (Thu):** No lab session
- **14th Dec (Tue):** Graduate student's presentation
- **16th Dec (Thu):** Graduate student's demo