

# CSCE 6730: Advanced VLSI Systems

Assignment # 5, Total Points = 100.

Assigned Date: 11 Nov 2010 (Thu), Due Date: 18 Nov 2010 (Thu)

Instructor: Dr. Saraju P. Mohanty

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1. Get the following paper from *IEEE Xplore*:

S. P. Mohanty, D. Ghai, and E. Kougianos, "A P4VT (Power-Performance-Process-Parasitic-Voltage-Temperature) Aware Dual- $V_{Th}$  Nano-CMOS VCO", in *Proceedings of the 23rd IEEE International Conference on VLSI Design*, pp. 99-104, 2010.

Read the article and write a 1-page review of it in your own words.