CSCE 5730/4730: Digital CMOS VLSI Design

Assignment # 1, Total Marks = 5*20 = 100. Assigned Date: 8th Sep 2010 (Wed), Due Date: 15th Sep 2010 (Wed) Instructor: Dr. Saraju P. Mohanty

- 1. Give a list of five leading journals in the area of VLSI, VLSI design automation. Provide their publishers name and also their homepage URL from publishers or editors.
- 2. Give a list of five leading conferences in the area of VLSI, VLSI design automation. Provide their native homepage URL and last two years (2009 and 2008) venue.
- 3. Give a list of five free and/or commercial CAD tools. Explain the capabilities of each one of these tools.
- 4. Find out 3 chips that have or will have more than billion transistors.
- 5. Comment on similarity and differences between CPU and GPU.