

CSCE 5730/4730: Digital CMOS VLSI Design

Assignment # 3, Total Points = $2*50 = 100$.

Assigned Date: 6th Oct 2010 (Wed), Due Date: 13th Oct 2010 (Wed)

Instructor: Dr. Saraju P. Mohanty

1. Synthesize the Boolean function $F = (A \cdot B' + C \cdot D)$. Show all the steps. Verify the circuit functionality using LTspice.
2. Design a D-latch using 45nm technology in LTspice. Perform simulations to prove its functionality and transparent property.