CSCE 5730/4730: Digital CMOS VLSI Design

Assignment # 4, Total Points = 2*50 = 100.
Assigned Date: 20th Oct 2010 (Wed), Due Date: 27th Oct 2010 (Wed)
Instructor: Dr. Saraju P. Mohanty

1. Design a D Flip-Flop using 45nm technology in LTspice. Perform simulations to prove its functionality.
2. Briefly explain the concepts of: V_{Dsat}, I_{Dsat}, V_{th}, mobility, trans-conductance.